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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gurtej S. Sandhu et al.

Examiner: Laura M Schillinger

Patent No.: 7,344,948

Group Art Unit: 2813

Issue Date: March 18, 2008

Docket No: 1999-0914.02/US

Title: METHODS OF FORMING TRANSISTORS

Commissioner for Patents  
Office of Patent Publication  
ATTN: Certificate of Correction Branch

**Certificate**  
**APR 02 2008**  
**of Correction**

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- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

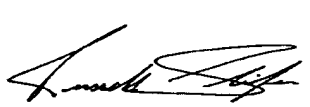
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Customer Number: 26809

Respectfully submitted,

Date: March 28, 2008

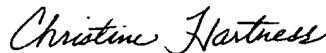
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Russ Slifer  
Reg. No. 39,838

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Christine Hartness

\_\_\_\_\_  
Name



\_\_\_\_\_  
Signature

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**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

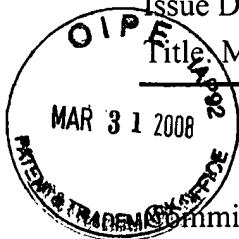
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Title: METHODS OF FORMING TRANSISTORS



**REQUEST FOR CERTIFICATION OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

**As the error is that of the Patent Office, it is believed that no fee is due.**

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

**Customer Number: 26809**

Date: March 28, 2008

By: \_\_\_\_\_

A handwritten signature in black ink, appearing to read "Russ Slifer".

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\_\_\_\_\_  
Name

\_\_\_\_\_  
Signature

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Patent Evaluation

APR 02 2008

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,344,948

Page (1) of 1

DATED : March 18, 2008

INVENTOR(S) : Sandhu et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (62), under "Related U.S. Application Data", in column 1, line 2, delete "2000." and insert - - 2000, now U.S. Patent No. 6,660,657. - -, therefor.

In column 1, line 7, delete "2000." and insert - - 2000, now U.S. Patent No. 6,660,657. - -, therefor.

In column 5, line 27, in Claim 1, after "upper" insert - - oxide-comprising - -.

In column 5, line 28, in Claim 1, after "lower" insert - - oxide-comprising - -.

In column 5, line 36, in Claim 1, delete "400°" and insert - - 400° C. - -, therefor.

In column 5, line 37, in Claim 1, delete "exposing,thermally" and insert - - exposing, thermally - -, therefor.

In column 6, line 3, in Claim 1, delete "silicon-oxide-containing" and insert - - gate oxide - -, therefor.

In column 6, line 5, in Claim 1, delete "at least one" and insert - - a - -, therefor.

In column 6, line 5, in Claim 1, delete "over" and insert - - on and in direct physical contact with the upper oxide-comprising surface of - -, therefor.

In column 6, line 19, in Claim 4, delete "500watts" and insert - - 500 watts - -, therefor.

In column 6, lines 26-27, in Claim 7, delete "The method of claim 1 wherein the exposing occurs for a time of less than or equal to about 1 minute." and insert - - The method of claim 1 wherein the annealing comprises thermal processing at temperature of less than 1100° C. for a time of at least 3 seconds. - -, therefor.

In column 6, line 28, in Claim 8, after "is" insert - - a - -.

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